

Berg
Serial no. 10/045,925
Filed 1/9/2002
Attorney docket no. BEA920000022US1

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In the specification:

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Please replace page 1, line 5, through page 2, line 11, with the following paragraphs.

U.S. patent application serial number 10/045,795 by T.B. Berg et al.
(BEA919990003US1) entitled "Method And Apparatus For Increasing Requestor Throughput By Using Data Available Withholding" was filed on January 9, 2002.

U.S. patent application serial number 10/045,927 by T.B. Berg et al.
(BEA920000017US1) entitled "Method And Apparatus For Using Global Snooping To Provide Cache Coherence To Distributed Computer Nodes In A Single Coherent System" was filed on January 9, 2002.

U.S. patent application serial number 10/045,821 by T.B. Berg et al.
(BEA920000018US1) entitled "Multi-level Classification Method For Transaction Address Conflicts For Ensuring Efficient Ordering In A Two-level Snoopy Cache Architecture" was filed on January 9, 2002.

U.S. patent application serial number 10/045,564 by S.G. Lloyd et al.
(BEA920000019US1) entitled "Transaction Redirection Mechanism For Handling Late Specification Changes And Design Errors" was filed on January 9, 2002.

U.S. patent application serial number 10/045,797 by T.B. Berg et al.
(BEA920000020US1) entitled "Method And Apparatus For Multi-path Data Storage And Retrieval" was filed on January 9, 2002.

U.S. patent application serial number 10/045,923 by W.A. Downer et al.
(BEA920000021US1) entitled "Hardware Support For Partitioning A Multiprocessor System To Allow Distinct Operating Systems" was filed on January 9, 2002.

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U.S. patent application serial number 10/045,925 by T.B. Berg et al. (BEA920000022US1) entitled "Distributed Allocation Of System Hardware Resources For Multiprocessor Systems" was filed on January 9, 2002.

U.S. patent application serial number 10/045,926 by W.A. Downer et al. (BEA920010030US1) entitled "Masterless Building Block Binding To Partitions" was filed on January 9, 2002.

U.S. patent application serial number 10/045,774 by W.A. Downer et al. (BEA920010031US1) entitled "Building Block Removal From Partitions" was filed on January 9, 2002.

U.S. patent application serial number 10/045,769 by W.A. Downer et al. (BEA920010041US1) entitled "Masterless Building Block Binding to Partitions Using Identifiers and Indicators" was filed on January 9, 2002.

Please replace the abstract on page 24 of the specification with the following abstraction paragraph.

~~A method of allocating hardware resources in a multiprocessor computer system which utilizes non-uniform memory access and distributed system resources across multiple nodes. The disclosure provides a~~ A method is provided ~~for allocating system resources across multiple nodes of a system communicating through a hardware device comprised of a tag and address crossbar system interconnecting node control devices. The method provides for allocation of transaction units or transaction identifiers in an allocating component for use in a multiple target component which may be in a distinct target node within the multiple node system. Based on the operations~~

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or requests that a target node receives from multiple external request source nodes, each requiring the use of target transaction unit objects such as transaction identification bits, the method provides inclusion of such information in the initial request to a target node which allows any data transmission between the source node and the target node, or the target node and the source node to be accomplished without any further intervention by the allocating component. Such component may be a local memory control agent or device. ~~The method allows for reduction of system latency by communicating the identification of the hardware resource target, allocated by a tag and address crossbar, through the system data crossbar by attaching such information to the data to be communicated to the resource.~~